

**AMENDMENTS TO THE CLAIMS**

1 (Currently Amended): An in-plane switching mode liquid crystal display device comprising:

first and second substrates having an array region and a sealant region along a periphery of the array region, wherein the array region includes a plurality of pixel regions defined by a plurality of gate lines and data lines on the second substrate and the sealant region includes a plurality of gate pads and data pads at an end of the gate and data lines;

a sealant in the sealant region attaching the first and second substrates, wherein the sealant is located over the gate and data pads;

a single metallic black matrix formed in the sealant region that extends into the array region of the first substrate, the single metallic black matrix having a first portion and a second portion in the sealant region;

a color filter on the metallic black matrix extending into the array region from the sealant region;

a common electrode and a pixel electrode on the second substrate in the array region;

an organic layer on the color filter in the array region, the organic layer covering at least a the first portion of the single metallic black matrix to shield an electric field in the array region, wherein the organic layer is formed in the array region and in the sealant region; and

a liquid crystal layer between the first and second substrates,

wherein the sealant is disposed in the second portion of the metallic black matrix and the organic layer is formed in a part area of the second portion of the metallic black matrix so that a part area of the metallic black matrix is exposed.

2 (Original): The device of claim 1, wherein the metallic black matrix is one of Cr and CrO<sub>x</sub>.

3 (Original): The device of claim 1, wherein the organic layer is formed in the array region.

4 (Original): The device of claim 3, wherein the organic layer is in direct contact with the metallic black matrix.

5 (Cancelled)

6 (Original): The device of claim 1, wherein the organic layer is in direct contact with the sealant.

7 (Cancelled)

8 (Currently Amended): A method for fabricating an in-plane switching mode liquid crystal display device, comprising:

providing first and second substrates having a sealant region and an array region, wherein the array region includes a plurality of pixels defined by a plurality of gate lines and data lines on the second substrate and the sealant region includes a plurality of gate pads and data pads at an end of the gate and data lines;

forming a single metallic black matrix in the sealant region that extends into the array region of the first substrate, the single metallic black matrix having a first portion and a second portion in the sealant region;

forming a color filter on the single metallic black matrix extending into the array region from the sealant region, wherein the organic layer is formed in the sealant region and the array region;

forming a pixel electrode and a common electrode on the second substrate in the array region;

forming an organic layer on the color filter in the array region, the organic layer covering ~~at least~~ the first portion of single the metallic black matrix to shield an electric field in the array region, wherein the organic layer is formed in the sealant region ~~and the array region;~~

forming a sealant over the gate and data pads of the sealant region; and

attaching the first and second substrates by the sealant,

wherein the sealant is disposed in the second portion of the metallic black matrix and the organic layer is formed in a part area of the second portion of the metallic black matrix so that a part area of the metallic black matrix is exposed.

9 (Original): The method of claim 8, wherein the metallic black matrix is one of Cr and CrO<sub>x</sub>.

10 (Original): The method of claim 8, wherein the organic layer is formed in the array region.

11 (Cancelled)

12 (Cancelled)

13 (Original): The method of claim 8, further comprising:  
forming a liquid crystal layer between the first and second substrates.

14 (Previously Presented): The device of claim 1, wherein the black matrix extends over at least one thin film transistor in the array region.

15 (Previously Presented): The device of claim 8, wherein the black matrix extends over at least one thin film transistor in the array region.